

# FOR APPROVAL

**MODEL NO.:**

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**Customer's Approved**

**Customer:**

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**Data:**

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**By:**

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|              |             |                |            |
|--------------|-------------|----------------|------------|
| Confirmed By | Prepared By | Manufacture NO |            |
|              |             | Version        | 1.0        |
|              |             | To tal Pages   | 6          |
|              |             | Date           | JUNE. 2004 |

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**MODEL No. : SVA025\_035LED\_TW\_V1.0**

**1.Features:**

Fit TFT-LCD PANEL : FG020511ANSWA-01/FG030511ANSWA-L1

Video input Compatible with PAL or NTSC system

**2.Mechanical specification:**

Board Dimension Unit

62.50mm\*50.50mm\*8.00 mm

**3.Video input signal:**

Composite Video signal 1.0Vp-p75

**4.Work temperature:**

-20<sup>0</sup>C—+50<sup>0</sup>C

**5.Electrical characteristics:**

| Parameter | Symbol | Min  | Typ   | Max   | Unit |
|-----------|--------|------|-------|-------|------|
| Supply    | Vcc    | +6.0 | +12.0 | +13.0 | Volt |

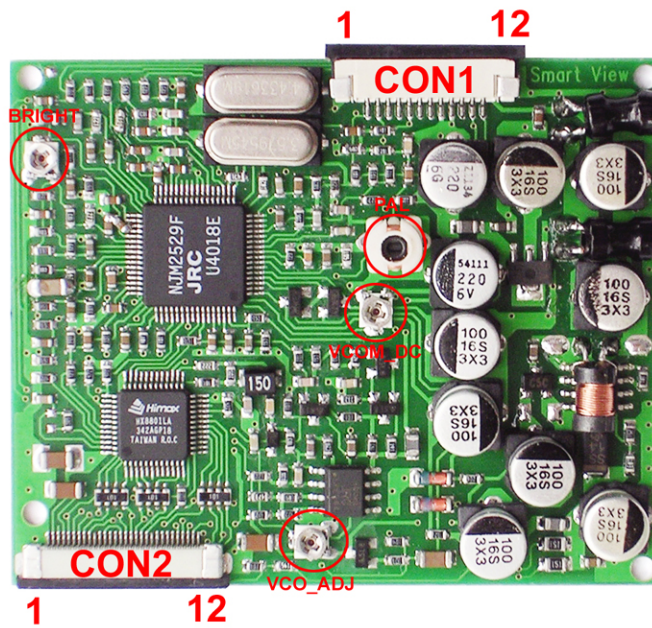
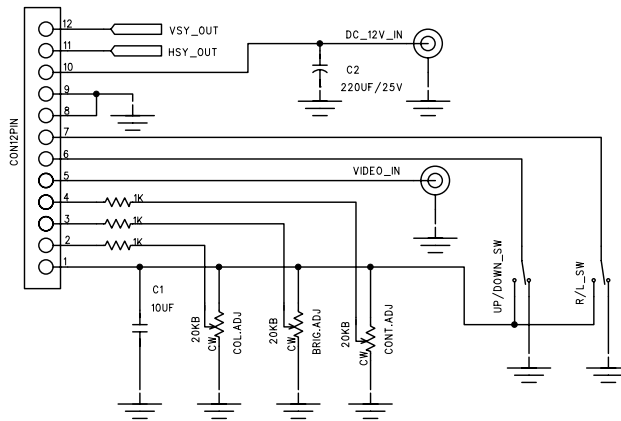
**6.Power consumption: 2.5"&3.5"**

| Parameter       | Condition | Min     | Typ     | Max     | Unit |
|-----------------|-----------|---------|---------|---------|------|
| Current for Vcc | VCC=+6 V  | 220/320 | 230/330 | 240/340 | mA   |
| Current for Vcc | VCC=+7 V  | 200/280 | 210/290 | 220/300 | mA   |
| Current for Vcc | VCC=+8 V  | 180/250 | 190/260 | 200/270 | mA   |
| Current for Vcc | VCC=+9 V  | 170/230 | 180/240 | 190/250 | mA   |
| Current for Vcc | VCC=+10V  | 160/220 | 170/230 | 180/240 | mA   |
| Current for Vcc | VCC=+11V  | 150/200 | 160/210 | 170/220 | mA   |
| Current for Vcc | VCC=+12V  | 140/190 | 150/200 | 160/210 | mA   |
| Current for Vcc | VCC=+13V  | 140/180 | 150/190 | 160/200 | mA   |
| Current for Vcc |           |         |         |         |      |
| Current for Vcc |           |         |         |         |      |
| Current for Vcc |           |         |         |         |      |
| Current for Vcc |           |         |         |         |      |
| Current for Vcc |           |         |         |         |      |
| Current for Vcc |           |         |         |         |      |

**7. Panel mechanical specification:**

| Parameter           | Specification                             | Unit |
|---------------------|---|------|
| Screen Size         | 2.5" 3.5" diagonal                        | inch |
| Display Format      | 480(H)x234(V)                             | dot  |
| Active Area         | 2.5"49.2Wx38.14Hx5.8T 3.5"83.7x68.6Hx8.1T | mm   |
| Dot Pitch           | 2.5" _0.1025Wx0.163H_3.5"0.149Hx0.225V)   | mm   |
| Color Configuration | R.G.B. delta                              |      |
|                     |   |      |

**8.Driver board terminal drawing:**



**8.1: CN1&CN3**

|     |  | CN1 |            |     |           |     |  |     |  |
|-----|--|-----|------------|-----|-----------|-----|--|-----|--|
| PIN |  | PIN |            | PIN |           | PIN |  | PIN |  |
|     |  | 1   | +5V OUT    | 10  | DC+12V IN |     |  |     |  |
|     |  | 2   | COL_ADJ    | 11  | HSY OUT   |     |  |     |  |
|     |  | 3   | BRIG_ADJ   | 12  | VSY OUT   |     |  |     |  |
|     |  | 4   | CONT_ADJ   |     |           |     |  |     |  |
|     |  | 5   | VIDEO IN   |     |           |     |  |     |  |
|     |  | 6   | UP/DOWN_SW |     |           |     |  |     |  |
|     |  | 7   | R/L_SW     |     |           |     |  |     |  |
|     |  | 8   | GND        |     |           |     |  |     |  |
|     |  | 9   | GND        |     |           |     |  |     |  |

**8.1: CN2**

| Pin No | Symbol           | I/O | Description  | Remark |
|--------|------------------|-----|--|--------|
| 1      | GND              |     | Ground of Gate Driver.                             |        |
| 2      | V <sub>CC</sub>  | I   | Logic power of Gate Driver                         |        |
| 3      | V <sub>GL</sub>  | I   | Gate off Voltage (Alternative Every 1-H)           |        |
| 4      | V <sub>GH</sub>  | I   | Gate on Voltage                                    |        |
| 5      | STVR             | I/O | Vertical Start Pulse input, when U/D=High          |        |
| 6      | STVL             | I/O | Vertical Start Pulse input, when U/D=LOW           |        |
| 7      | CKV              |     | Shift clock input for gate driver                  |        |
| 8      | U/D              | I   | Up / Down Control for Gate Driver                  |        |
| 9      | OEV              | I   | Output enable for Gate driver                      |        |
| 10     | V <sub>COM</sub> | I   | Common Electrode Voltage                           |        |
| 11     | V <sub>COM</sub> | I   | Common Electrode Voltage                           |        |
| 12     | GLED1            | I   | Ground of LED 1.                                   |        |
| 13     | VLED1            | I   | Voltage of LED 1.                                  |        |
| 14     | VLED2            | I   | Voltage of LED 2.                                  |        |
| 15     | GLED2            | I   | Ground of LED 2.                                   |        |
| 16     | L/R              | I   | Left / Right for Source Driver                     |        |
| 17     | Q1H              |     | Analog signal rotate input.                        |        |
| 18     | OEH              |     | Output enable for Source driver                    |        |
| 19     | STHL             | I/O | Start Pulse for Source Driver input, when L/R=High |        |
| 20     | STHR             | I/O | Start Pulse for Source Driver input, when L/R=LOW  |        |
| 21     | CPH3             | I   | Sampling and Shift Clock for Source Driver         |        |



9.Driver board outline drawing:

