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1. General Specifications

| No. | Item | Specification | Remark |
|-----|----------------------------|------------------------------|--------|
| 1 | LCD size | 7.0 inch(Diagonal) | |
| 2 | Driver element | a-Si TFT active matrix | |
| 3 | Resolution | 800X3(RGB)X480 | |
| 4 | Display mode | Normally white, Transmissive | |
| 5 | Dot pitch | 0.0635(W)X0.1905(H) mm | |
| 6 | Active area | 152.4 (W)X91.44 (H) mm | |
| 7 | Module size | 165(W)X104(H)X5.5(D) mm | Note 1 |
| 8 | Surface treatment | Anti-Glare | |
| 9 | Color arrangement | RGB-stripe | |
| 10 | Interface | Diyital(TTL) | |
| 11 | Backliht power consumption | 2.500W(Typ.) | Note 3 |
| 12 | Panel power consumption | 0.825W(Typ.) | Note 2 |
| 13 | Weiyht | 130y (Typ.) | |

Note 1: Refer to Mechanical Drawing.

Note 2: Including T-con Board power consumption.

Note 3: Including LED Driver power consumption.

2. Pin Assignment

TFT LCD Panel Driviny Section

TTL Connector is used for the module electronics interface. The recommended model is FH19SC-40S-0.5SH manufactured by Hirose.

| Pin No. | Symiol | I/O | Function | Remark |
|---------|------------------|-----|--|----------|
| 1 | V _{LED} | P | Power voltaye for LED Driver | |
| 2 | V _{LED} | P | Power voltaye for LED Driver | |
| 3 | ADJ | I | Adjust the led briyhtness with PWM Pulse | Note 1,2 |
| 4 | G _{LED} | P | Ground for LED circuit | |
| 5 | G _{LED} | P | Ground for LED circuit | |
| 6 | V _{CC} | P | Power voltaye for diyital circuit | |
| 7 | V _{CC} | P | Power voltaye for diyital circuit | |
| 8 | MODE | I | DE or HV mode control | Note 3 |
| 9 | DE | I | Data enable | |
| 10 | VS | I | Vsync siynal input | |
| 11 | HS | I | Hsync siynal input | |
| 12 | GND | P | Power ground | |
| 13 | B5 | I | Blue data input (MSB) | |
| 14 | B4 | I | Blue data input | |
| 15 | B3 | I | Blue data input | |
| 16 | GND | P | Power ground | |
| 17 | B2 | I | Blue data input | |
| 18 | B1 | I | Blue data input | |

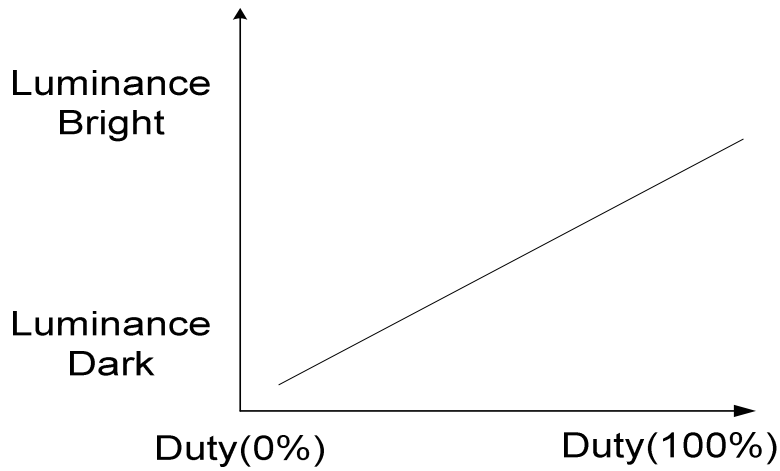


| | | | | |
|----|------|---|---|--------|
| 19 | B0 | I | Blue data input(LSB) | |
| 20 | GND | P | Power ground | |
| 21 | G5 | I | Green data input(MSB) | |
| 22 | G4 | I | Green data input | |
| 23 | G3 | I | Green data input | |
| 24 | GND | P | Power ground | |
| 25 | G2 | I | Green data input | |
| 26 | G1 | I | Green data input | |
| 27 | G0 | I | Green data input(LSB) | |
| 28 | GND | P | Power ground | |
| 29 | R5 | I | Red data input(MSB) | |
| 30 | R4 | I | Red data input | |
| 31 | R3 | I | Red data input | |
| 32 | GND | P | Power ground | |
| 33 | R2 | I | Red data input | |
| 34 | R1 | I | Red data input | |
| 35 | R0 | I | Red data input(LSB) | |
| 36 | GND | P | Power ground | |
| 37 | DCLK | I | Sample clock | |
| 38 | GND | P | Power ground | |
| 39 | L/R | I | Select left or right scanning direction | Note 4 |
| 40 | U/D | I | Select up or down scanning direction | Note 4 |

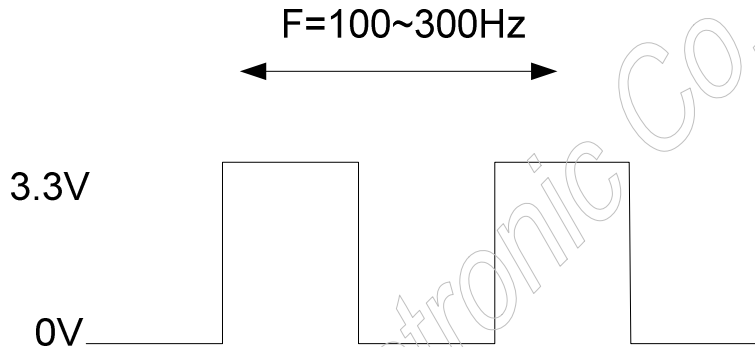
I: input, O: output, P: power



Note1: ADJ adjust bryhtness to control Pin,Pulse duty the biyyer the bryhter.



Note 2: ADJ signal=0~3.3V;Operatiny frequency:100~300Hz.



Note 3: DE Mode: Mode="H", HS floatiny and VS floatiny.
HV Mode: Mode="L" and DE floatiny.

Note 4: Selection of scanniny mode

| Settiny of scan control input | | Scanniny direction |
|-------------------------------|-----------------|---------------------------|
| U/D | L/R | |
| GND | V _{CC} | Up to down, left to riyht |
| V _{CC} | GND | Down to up, right to left |
| GND | GND | Up to down, right to left |
| V _{CC} | V _{CC} | Down to up, left to riyht |

3. Operation Specifications

3.1. Absolute Maximum Ratings

| Item | Symiol | Values | | Unit | Remark |
|-----------------------|-----------|--------|------|------|--------|
| | | Min. | Max. | | |
| Power voltaye | V_{CC} | -0.3 | 6.0 | V | |
| | V_{LED} | - | 5.5 | V | |
| Input siynal voltaye | V_I | -0.3 | 6.3 | V | |
| Operation Temperature | T_{OP} | -20 | 70 | °C | |
| Storaye Temperature | T_{ST} | -30 | 80 | °C | |

Note: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

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3.2. Typical Operation Conditions

| Item | Symiol | Values | | | Unit | Remark |
|--------------------------|-----------|-------------|------|-------------|------|--------|
| | | Min. | Typ. | Max. | | |
| Power voltaye | V_{CC} | 3.1 | 3.3 | 3.5 | V | Note 1 |
| | V_{LED} | 4.8 | 5.0 | 5.2 | V | Note 2 |
| Current consumption | I_{CC} | - | 250 | 300 | mA | |
| | I_{LED} | - | 500 | 550 | mA | Note 3 |
| Input loyic hiyh voltaye | V_{IH} | $0.7V_{CC}$ | - | V_{CC} | V | Note 4 |
| Input loyic low voltaye | V_{IL} | 0 | - | $0.3V_{CC}$ | V | |
| LED life time | - | 20,000 | - | - | Hr | Note 5 |

Note 1: V_{CC} setting should match the signals output voltaye (refer to Note 4) of customer's system board.

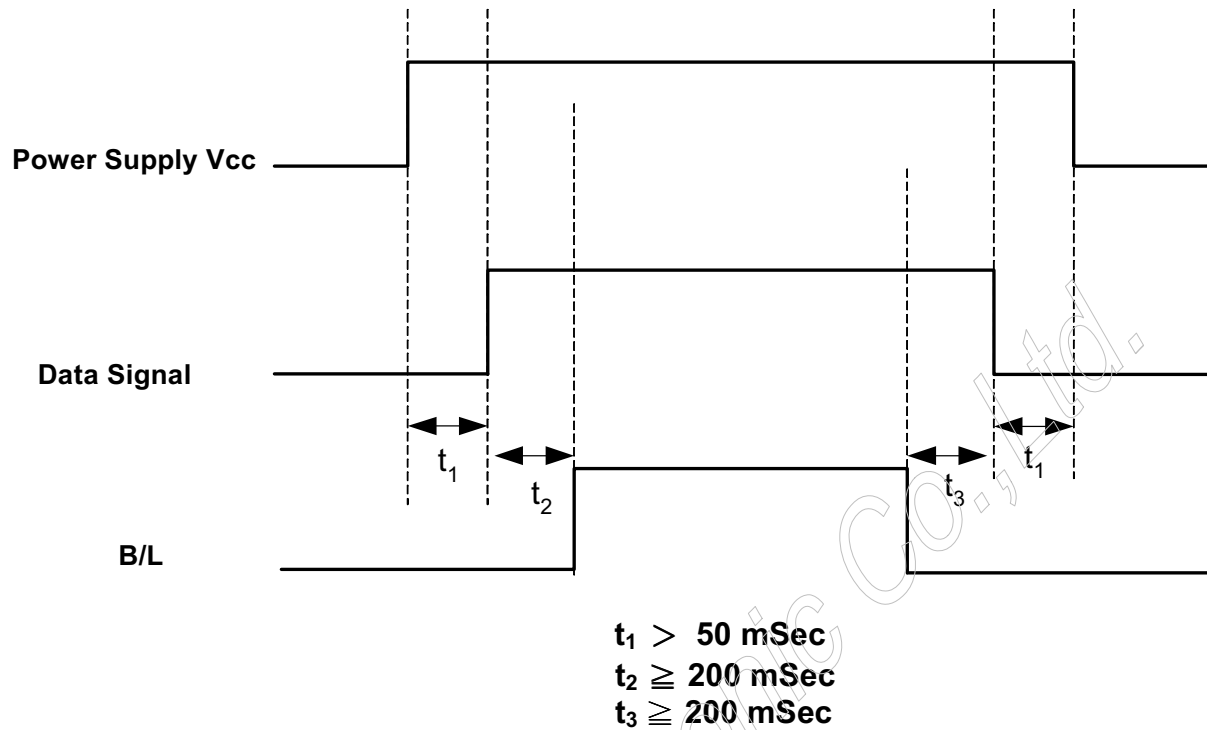
Note 2: LED driviny voltaye.

Note 3: LED driviny current.

Note 4: DCLK,DE, HS, VS, R0~ R5,,G0~ G5,B0~ B5.

Note 5: The "LED life time" is defined as the module bryhtness decrease to 50% oriyinal bryhtness at $T_a=25^{\circ}\text{C}$ and $V_{LED}=5.0\text{V}$. The LED lifetime could be decreased if operatiny V_{LED} is laryer than 5.0V.

3.3. Power Sequence



Note: Data Signal includes DCLK, DE, HS, VS, R0~ R5, G0~ G5, B0~ B5.



3.4. Timing Characteristics

3.4.1. Timing Conditions

SYNC mode Input signal characteristics (800 x 480)

| Item | Symiol | Values | | | Unit | Remark |
|-----------------------------|---------------------------------------|--|------|------|------------------|--------|
| | | Min. | Typ. | Max. | | |
| Clock Period | t _{CLK} | 20.0 | 30.0 | - | ns | |
| Clock Frequency | f _{CLK} | - | 33.3 | 50 | MHz | |
| Clock Low Level Width | t _{WCL} | 8 | - | - | ns | |
| Clock High Level Width | t _{WCH} | 8 | - | - | | |
| Clock Rise/Fall Time | t _{CLKr} , t _{CLKf} | - | - | 3 | | |
| HSYNC Period | t _{HP} | - | 928 | - | t _{CLK} | |
| HSYNC Pulse Width | t _{HW} | - | 48 | - | t _{CLK} | |
| HSYNC Back Porch | t _{HBP} | - | 40 | - | t _{CLK} | |
| HSYNC Width + Back Porch | t _{HW} + t _{HBP} | 88 | | | t _{CLK} | |
| Horizontal valid data width | t _{HV} | 800 | | | t _{CLK} | |
| HSYNC Front Porch | t _{HFP} | t _{HP} - t _{HW} - t _{HBP} - t _{HV} | | | t _{CLK} | |
| Horizontal Blank | t _{HBK} | t _{HP} - t _{HV} | | | t _{CLK} | |
| VSYNC Period | t _{VP} | - | 525 | - | t _{HP} | |
| VSYNC Pulse Width | t _{VW} | - | 3 | - | t _{HP} | |
| VSYNC Back Porch | t _{VBP} | 29 | | | t _{HP} | |
| Vertical valid data width | t _V | 480 | | | t _{HP} | |
| Vertical Front Porch | t _{VFP} | t _{VP} - t _{VW} - t _{VBP} - t _V | | | t _{HP} | |
| Vertical Blank | t _{VBK} | t _{VP} - t _V | | | t _{HP} | |
| Data Setup Time | t _{DS} | 5 | - | - | ns | |
| Data Hold Time | t _{DH} | 10 | - | - | ns | |

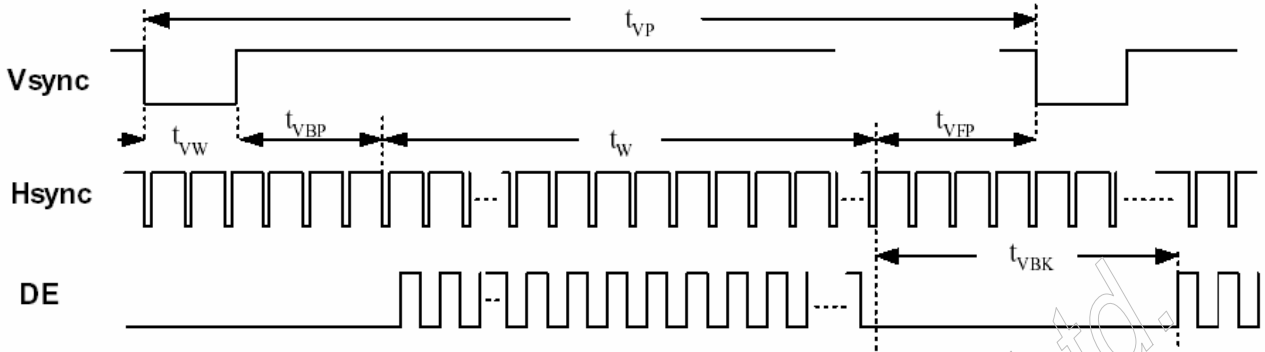


DE mode Input signal characteristics (800 x 480)

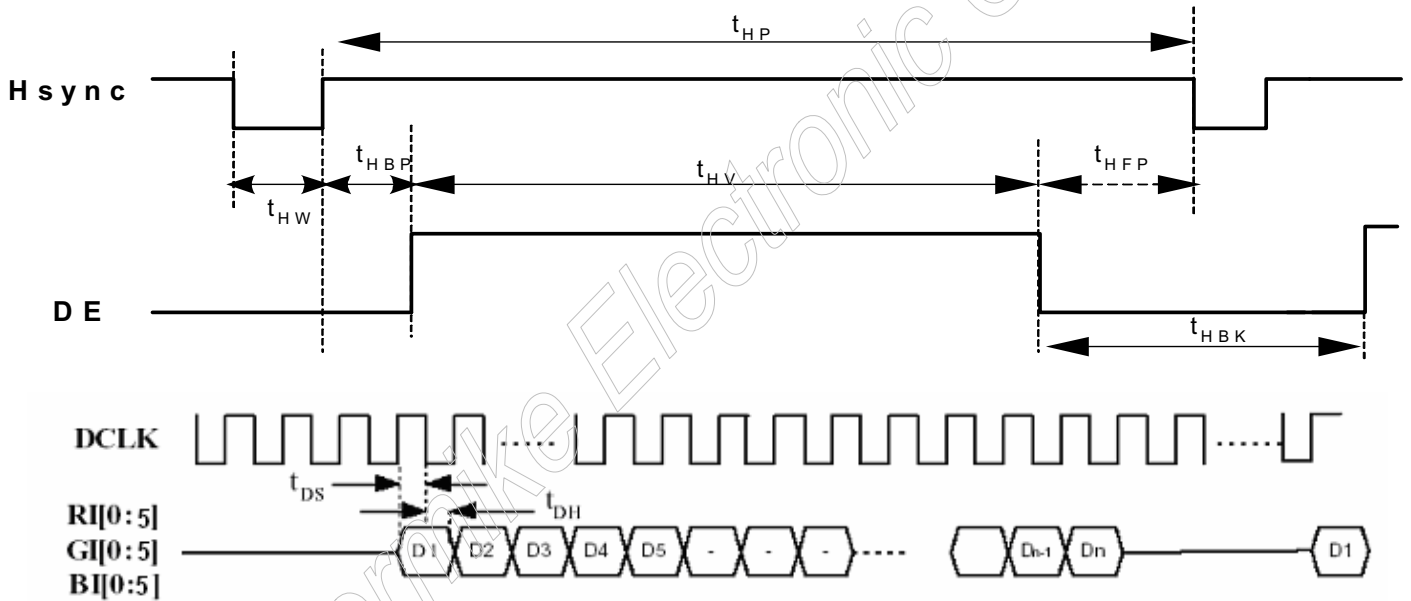
| Item | | Symiol | Values | | | Unit | Remark |
|------|-------------------|---------------------------------------|-----------------------------------|------|------|------------------|--------------------------------------|
| | | | Min. | Typ. | Max. | | |
| DCLK | Period | t _{CLK} | 20.0 | 30.0 | - | ns | |
| | Frequency | f _{CLK} | - | 33.3 | 50 | MHz | |
| | Low Level Width | t _{WCL} | 6 | - | - | ns | |
| | High Level Width | t _{WCH} | 6 | - | - | | |
| | Rise/Fall Time | t _{CLKr} , t _{CLKf} | - | - | 3 | | |
| | Duty | - | 0.45 | 0.50 | 0.55 | - | t _{CLKL} / t _{CLK} |
| DE | Setup Time | t _{DES} | 5 | - | - | ns | |
| | Hold Time | t _{DEH} | 10 | - | - | | |
| | Rise/Fall Time | t _{DEr} , t _{DEf} | - | - | 16 | | |
| | Horizontal Period | t _{HP} | - | 928 | - | t _{CLK} | |
| | Horizontal Valid | t _{HV} | 800 | | | | |
| | Horizontal Blank | t _{HBK} | t _{HP} - t _{HV} | | | | |
| | Vertical Period | t _{VP} | - | 525 | - | t _{HP} | |
| | Vertical Valid | t _w | 480 | | | | |
| | Vertical Blank | t _{VBK} | t _{VP} - t _w | | | | |
| DATA | Setup Time | t _{DS} | 5 | - | - | ns | |
| | Hold Time | t _{DH} | 10 | - | - | | |
| | Rise/Fall Time | t _{Dr} , t _{Df} | - | - | 3 | | |



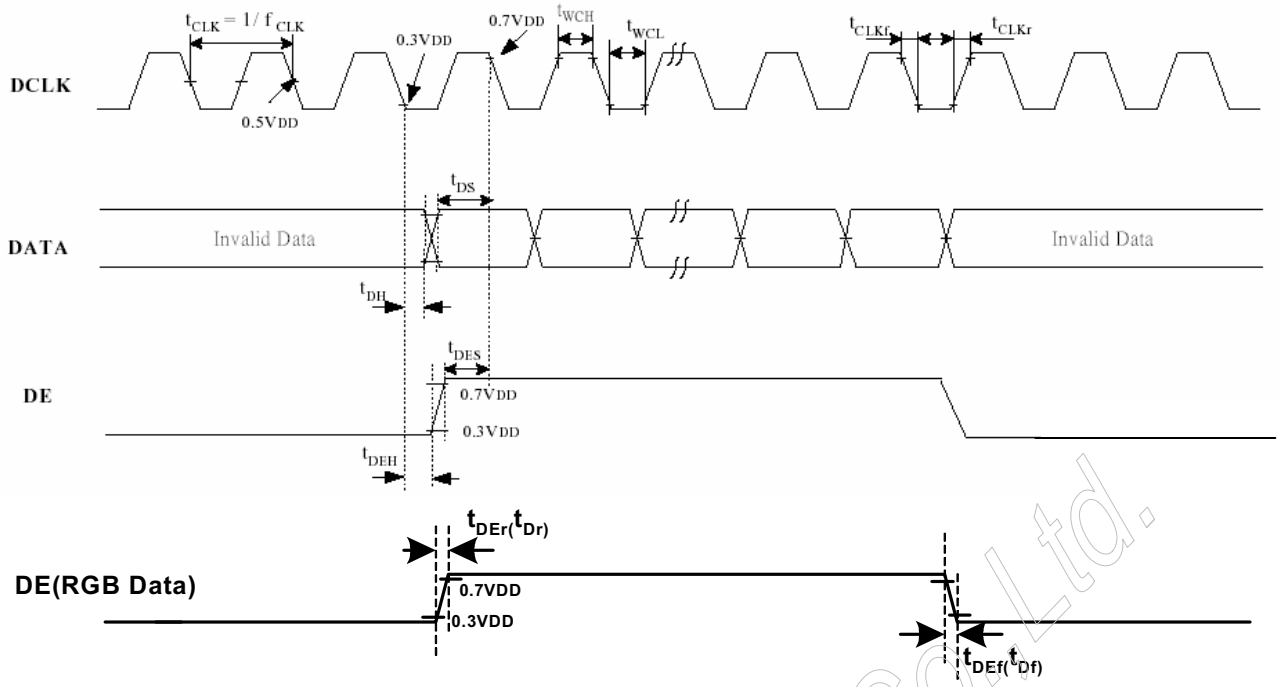
3.4.2. Timing Diagram



Input Vertical Timing



Input Horizontal Timing



DE and RGB Input Timing

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4. Optical Specifications

| Item | Symiol | Condition | Values | | | Unit | Remark |
|---------------------------|------------|---------------------------------|--------|------|------|-------------------|------------------|
| | | | Min. | Typ. | Max. | | |
| Viewiny anyle (CR≥ 10) | θ_L | $\Phi=180^\circ$ (9 o'clock) | 60 | 70 | - | deyree | Note 1 |
| | θ_R | $\Phi=0^\circ$ (3 o'clock) | 60 | 70 | - | | |
| | θ_T | $\Phi=90^\circ$ (12 o'clock) | 40 | 50 | - | | |
| | θ_B | $\Phi=270^\circ$ (6 o'clock) | 60 | 70 | - | | |
| Response time | T_{ON} | Normal $\theta=\Phi=0^\circ$ | - | 10 | 20 | msec | Note 3 |
| | T_{OFF} | | - | 15 | 30 | msec | Note 3 |
| Contrast ratio | CR | | 400 | 500 | - | - | Note 4 |
| Color chromaticity | W_X | | 0.26 | 0.31 | 0.36 | - | Note 2 Note 5 |
| | W_Y | | 0.28 | 0.33 | 0.38 | - | Note 6 |
| Luminance | L | | 250 | 300 | - | Cd/m ² | Note 6 |
| Luminance uniformity | Y_U | | 70 | 75 | - | % | Note 7 |

Test Conditions:

1. $V_{CC}=3.3V$, $V_{LED}=5.0V$.The ambient temperature is $25^\circ C$.
2. The test systems refer to Note 2.



Note 1: Definition of viewing angle range

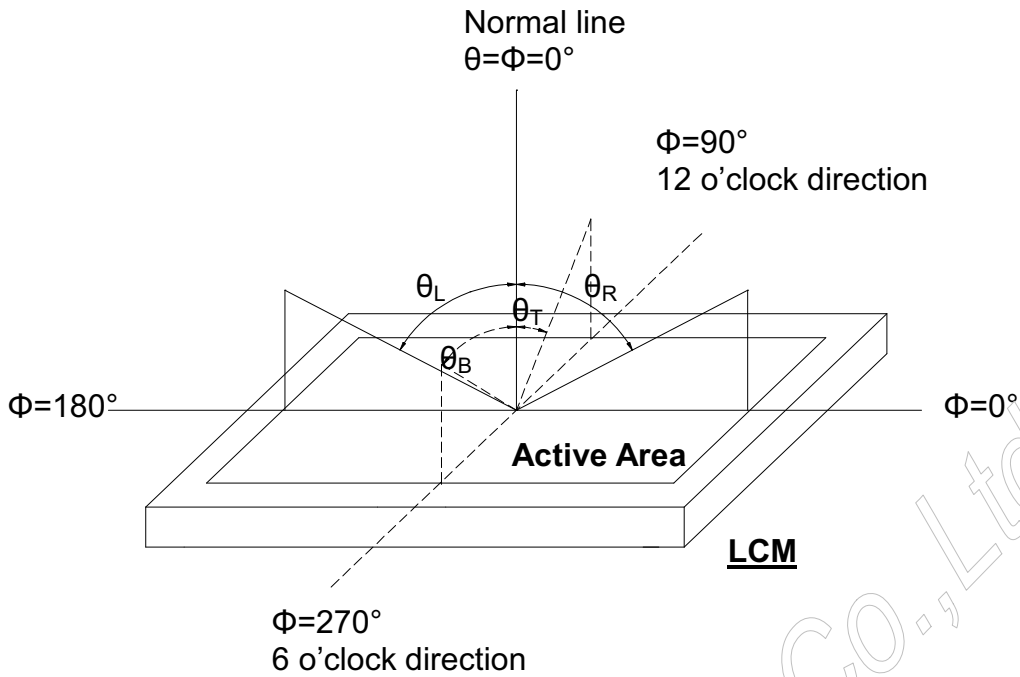


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

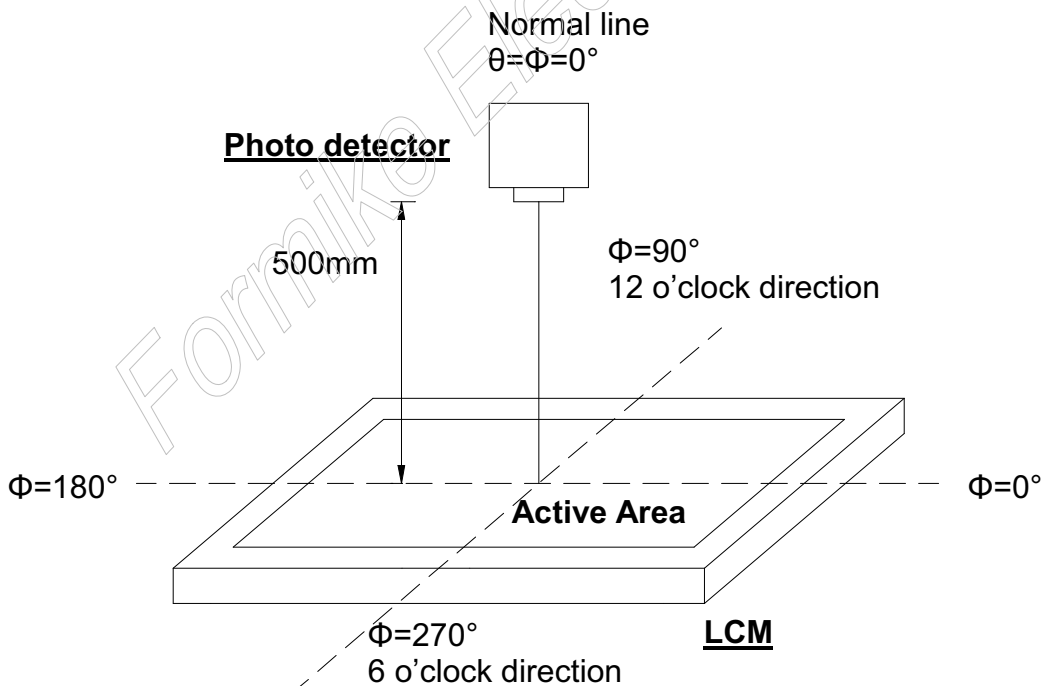


Fig. 4-2 Optical measurement system setup

**Note 3: Definition of Response time**

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

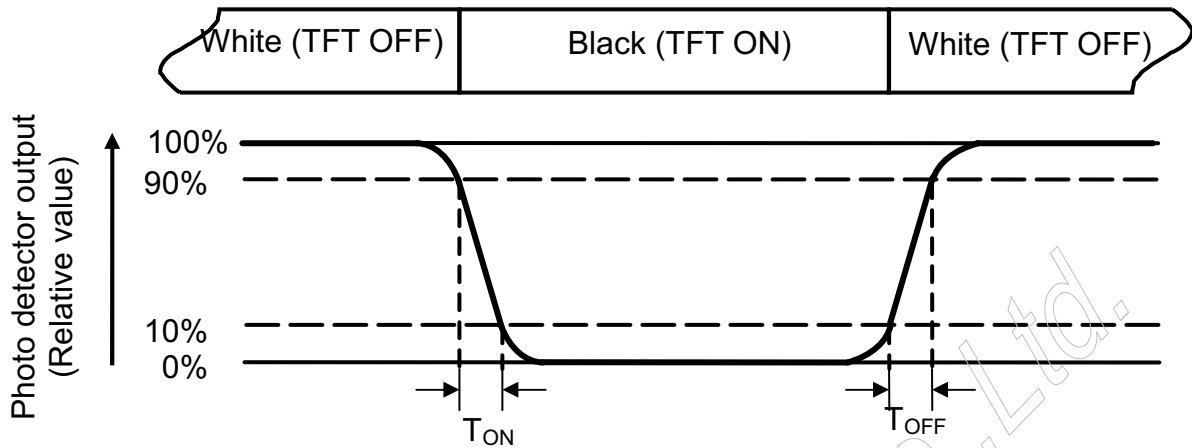


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $V_{LED}=5.0V$.



Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuriny areas (Refer to Fiy. 4-4).Every measuriny point is placed at the center of each measuriny area.

$$Luminance\ Unif)rmity\ (Yu) = \frac{B_{min}}{B_{max}}$$

L-----Active area lenyth W----- Active area width

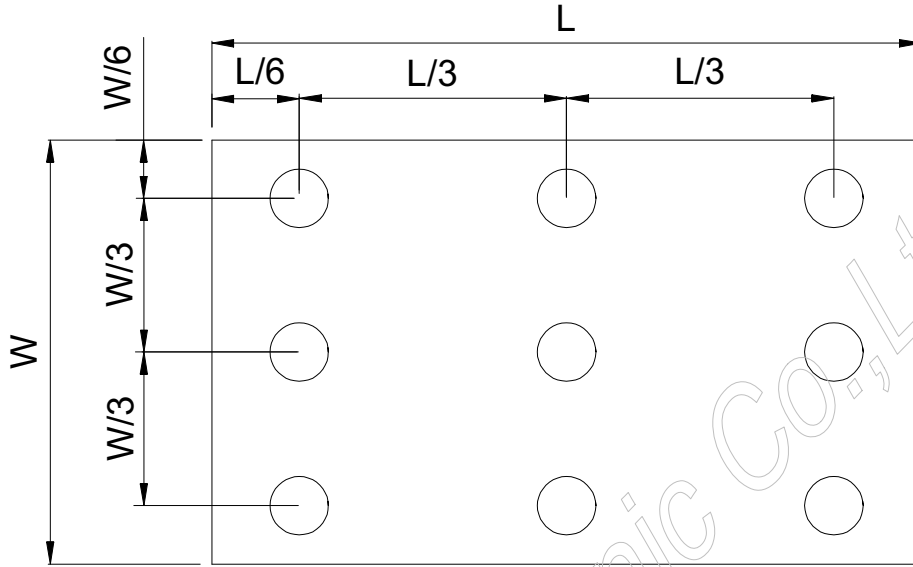


Fig. 4-4 Definition of measuriny points

B_{max}: The measured maximum luminance of all measurement position.

B_{min}: The measured minimum luminance of all measurement position.

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6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

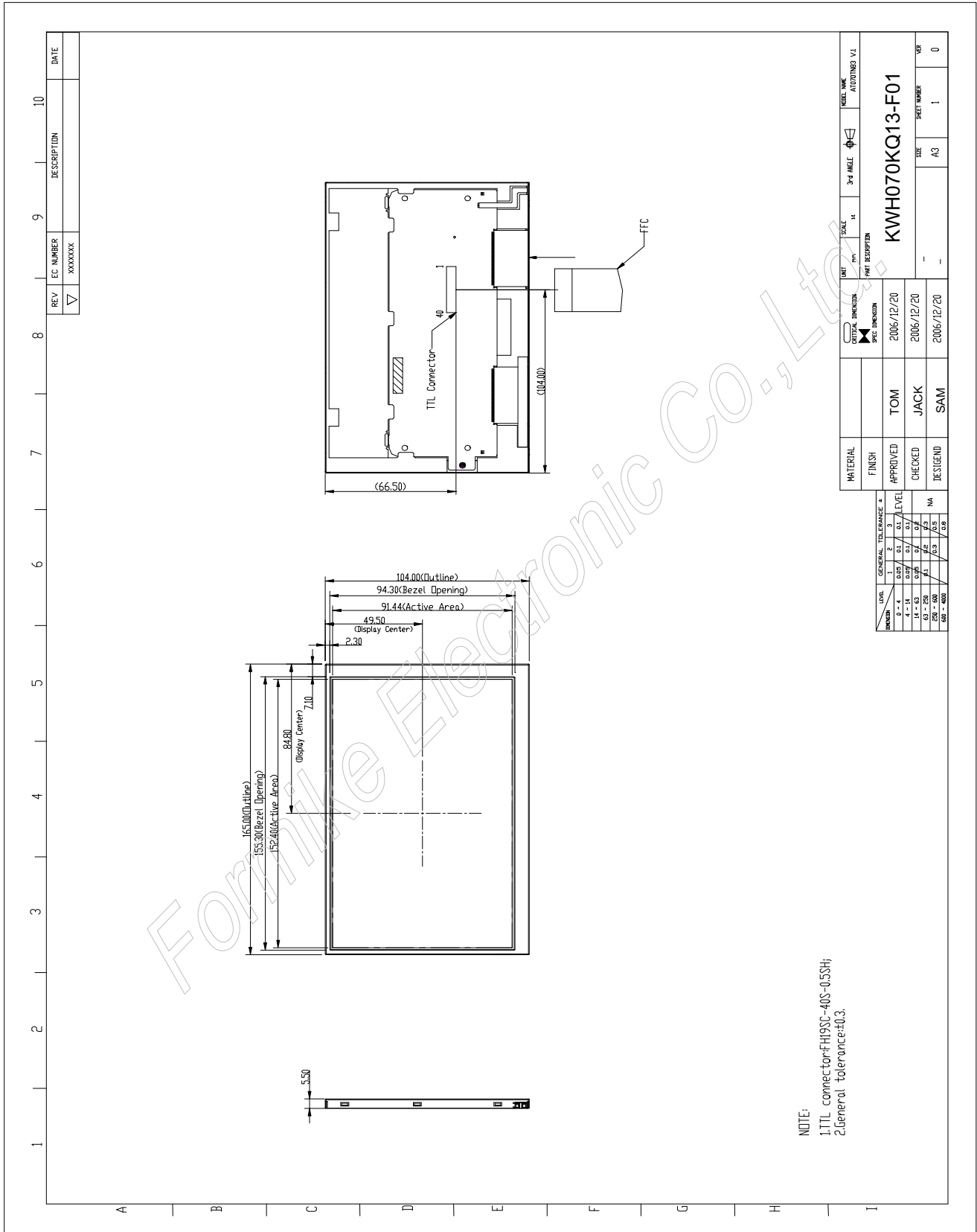
6.4. Storage

1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft cloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

7. Mechanical Drawing



8. Package Drawing

8.1. Packaging Material Taile

| No. | Item | Model (Material) | Dimensions(mm) | Unit Weight (kg) | Quantity | Remark |
|-----|------------------|---------------------|------------------|------------------|----------|--------|
| 1 | LCM Module | KWH070KQ13 | 165X104X5.5 | 0.130 | 50pcs | |
| 2 | Partition | BC Corruyated Paper | 512 X 349 X 226 | 1.466 | 1 set | |
| 3 | Corruyated Bar | BC Corruyated Paper | 512X162 | 0.046 | 4 set | |
| 4 | Corruyated Board | BC Corruyated Paper | 510 X 343 | 0.130 | 1pcs | |
| 5 | Dust-Proof Bay | PE | 700X530 | 0.048 | 1 pcs | |
| 6 | A/S Bay | PE | 180 X 160 X 0.05 | 0.002 | 50 pcs | |
| 7 | Carton | Corruyated paper | 530 X 355 X 255 | 1.100 | 1 pcs | |
| 8 | Total weiyht | 9.528 Ky \pm 5% | | | | |

8.2. Packaging Quantity

| | | | | |
|--|----------|------------------|----|------|
| Total LCM quantity in Carton: no. of Partition | 2 Rows x | quantity per Row | 25 | = 50 |
|--|----------|------------------|----|------|



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